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PTO/SB/21 (09-04)


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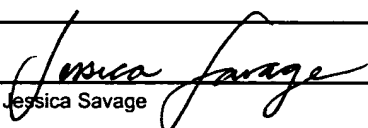
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<b>TRANSMITTAL FORM</b>  <small>(to be used for all correspondence after initial filing)</small>	Application Number	10/577,579
	Filing Date	27 April 2006
	First Named Inventor	Xiang Ma
	Art Unit	
	Examiner Name	
Total Number of Pages in This Submission	30	Attorney Docket Number 42P22768

ENCLOSURES (Check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT			
Firm Name	BLAKELY, SOKOLOFF, TAYLOR AND ZAFMAN, LLP 12400 Wilshire Boulevard, Seventh Floor, Los Angeles, CA 90025-1030		
Signature			
Printed name	Lester J. Vincent		
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Typed or printed name	Jessica Savage	Date	24 May 2006

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**证 明**  
**CERTIFICATE**

本证明之附件是向中国专利局作为受理局提交的下列国际申请副本  
THIS IS TO CERTIFY THAT ANNEXED HERETO IS A TRUE COPY OF THE BELOW  
IDENTIFIED INTERNATIONAL APPLICATION THAT WAS FILED WITH THE  
CHINESE PATENT OFFICE AS RECEIVING OFFICE

国际申请号: PCT/CN2005/001961

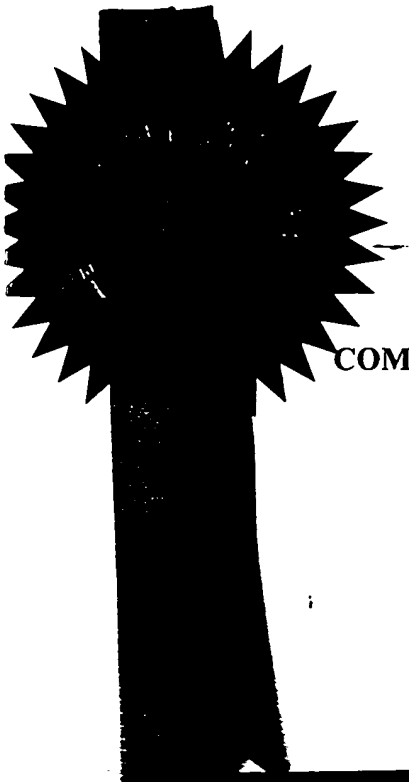
INTERNATIONAL APPLICATION NUMBER

国际申请日: 18. NOV 2005(18.11.2005)

INTERNATIONAL FILING DATE

发明名称: OPERATING MEDIA DEVICES IN PRE-OS ENVIRONMENT

TITLE OF INVENTION



中华人民共和国国家知识产权局局长  
COMMISSIONER OF THE STATE INTELLECTUAL PROPERTY  
OFFICE OF THE PEOPLE'S REPUBLIC OF CHINA

二零零六年四月十日

APRIL 10, 2006

**PCT****REQUEST**

The undersigned requests that the present international application be processed according to the Patent Cooperation Treaty.

For receiving Office use only

PCT/CN 2005 / 001961  
International Application No.18 · NOV 2005 (18 · 11 · 2005)  
International Filing DateRO/CN 中华人民共和国国家知识产权局  
PCT International Application

Name of receiving Office and "PCT International Application"

Applicant's or agent's file reference  
(if desired) (12 characters maximum) FPEL05150060

<b>Box No. I TITLE OF INVENTION</b> OPERATING MEDIA DEVICES IN PRE-OS ENVIRONMENT	
<b>Box No. II APPLICANT</b> <input type="checkbox"/> This person is also inventor	
Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country. The country of the address indicated in this Box is the applicant's State (that is, country) of residence if no State of residence is indicated below.) INTEL CORPORATION 2200 Mission College Blvd. Santa Clara, California 95052 United States of America	Telephone No.  Facsimile No.  Teleprinter No.  Applicant's registration No. with the Office
State (that is, country) of nationality: US	State (that is, country) of residence: US
This person is applicant for the purposes of: <input type="checkbox"/> all designated States <input checked="" type="checkbox"/> all designated States except the United States of America <input type="checkbox"/> the United States of America only <input type="checkbox"/> the States indicated in the Supplemental Box	
<b>Box No. III FURTHER APPLICANT(S) AND/OR (FURTHER) INVENTOR(S)</b>	
Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country. The country of the address indicated in this Box is the applicant's State (that is, country) of residence if no State of residence is indicated below.) MA, Xiang N20, Apt. 501, 400 Gui Lin West Road Shanghai 200063 P. R. of China	This person is: <input type="checkbox"/> applicant only <input checked="" type="checkbox"/> applicant and inventor <input type="checkbox"/> inventor only (If this check-box is marked, do not fill in below.)  Applicant's registration No. with the Office
State (that is, country) of nationality: CN	State (that is, country) of residence: CN
This person is applicant for the purposes of: <input type="checkbox"/> all designated States <input type="checkbox"/> all designated States except the United States of America <input checked="" type="checkbox"/> the United States of America only <input type="checkbox"/> the States indicated in the Supplemental Box	
<input checked="" type="checkbox"/> Further applicants and/or (further) inventors are indicated on a continuation sheet.	
<b>Box No. IV AGENT OR COMMON REPRESENTATIVE; OR ADDRESS FOR CORRESPONDENCE</b>	
The person identified below is hereby/has been appointed to act on behalf of the applicant(s) before the competent International Authorities as: <input checked="" type="checkbox"/> agent <input type="checkbox"/> common representative	
Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country.) China Patent Agent (H.K.) Ltd. 22/F, Great Eagle Centre 23 Harbour Road, Wanchai Hong Kong Special Administrative Region The People's Republic of China	Telephone No. (852)28284688 Facsimile No. (852)28271018 Teleprinter No.  Agent's registration No. with the Office
<input type="checkbox"/> Address for correspondence: Mark this check-box where no agent or common representative is/has been appointed and the space above is used instead to indicate a special address to which correspondence should be sent.	

**SUBSTITUTE SHEET**

Sheet No. ...2...

**Continuation of Box No. III FURTHER APPLICANT(S) AND/OR (FURTHER) INVENTOR(S)***If none of the following sub-boxes is used, this sheet should not be included in the request.*

Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country. The country of the address indicated in this Box is the applicant's State (that is, country) of residence if no State of residence is indicated below.)

Li, Yufu  
 RM 602, No. 8,  
 Lane 675, Sanlin Road  
 Shanghai 200000  
 P. R. of China

This person is:

- ☐ applicant only  
☒ applicant and inventor  
☐ inventor only (If this check-box is marked, do not fill in below.)

Applicant's registration No. with the Office

State (that is, country) of nationality:  
 CN

State (that is, country) of residence:  
 CN

This person is applicant for the purposes of:

- ☐ all designated States ☐ all designated States except the United States of America ☒ the United States of America only ☐ the States indicated in the Supplemental Box

Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country. The country of the address indicated in this Box is the applicant's State (that is, country) of residence if no State of residence is indicated below.)

This person is:

- ☐ applicant only  
☐ applicant and inventor  
☐ inventor only (If this check-box is marked, do not fill in below.)

Applicant's registration No. with the Office

State (that is, country) of nationality:

State (that is, country) of residence:

This person is applicant for the purposes of:

- ☐ all designated States ☐ all designated States except the United States of America ☐ the United States of America only ☐ the States indicated in the Supplemental Box

Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country. The country of the address indicated in this Box is the applicant's State (that is, country) of residence if no State of residence is indicated below.)

This person is:

- ☐ applicant only  
☐ applicant and inventor  
☐ inventor only (If this check-box is marked, do not fill in below.)

Applicant's registration No. with the Office

State (that is, country) of nationality:

State (that is, country) of residence:

This person is applicant for the purposes of:

- ☐ all designated States ☐ all designated States except the United States of America ☐ the United States of America only ☐ the States indicated in the Supplemental Box

Name and address: (Family name followed by given name; for a legal entity, full official designation. The address must include postal code and name of country. The country of the address indicated in this Box is the applicant's State (that is, country) of residence if no State of residence is indicated below.)

This person is:

- ☐ applicant only  
☐ applicant and inventor  
☐ inventor only (If this check-box is marked, do not fill in below.)

Applicant's registration No. with the Office

State (that is, country) of nationality:

State (that is, country) of residence:

This person is applicant for the purposes of:

- ☐ all designated States ☐ all designated States except the United States of America ☐ the United States of America only ☐ the States indicated in the Supplemental Box

☐ Further applicants and/or (further) inventors are indicated on another continuation sheet.

**SUBSTITUTE SHEET**

Sheet No. 3

**Box No. V DESIGNATIONS**

The filing of this request constitutes under Rule 4.9(a), the designation of all Contracting States bound by the PCT on the international filing date, for the grant of every kind of protection available and, where applicable, for the grant of both regional and national patents.

However,

- ☐ DE Germany is not designated for any kind of national protection
- ☐ KR Republic of Korea is not designated for any kind of national protection
- ☐ RU Russian Federation is not designated for any kind of national protection

*(The check-boxes above may be used to exclude (irrevocably) the designations concerned in order to avoid the ceasing of the effect, under the national law, of an earlier national application from which priority is claimed. See the Notes to Box No. V as to the consequences of such national law provisions in these and certain other States.)*

**Box No. VI PRIORITY CLAIM**

The priority of the following earlier application(s) is hereby claimed:

Filing date of earlier application (day/month/year)	Number of earlier application	Where earlier application is:		
		national application: country or Member of WTO	regional application:* regional Office	international application: receiving Office
item (1)				
item (2)				
item (3)				

☐ Further priority claims are indicated in the Supplemental Box.

The receiving Office is requested to prepare and transmit to the International Bureau a certified copy of the earlier application(s) (only if the earlier application was filed with the Office which for the purposes of this international application is the receiving Office) identified above as:

☐ all items      ☐ item (1)      ☐ item (2)      ☐ item (3)      ☐ other, see Supplemental Box

\* Where the earlier application is an ARIPO application, indicate at least one country party to the Paris Convention for the Protection of Industrial Property or one Member of the World Trade Organization for which that earlier application was filed (Rule 4.10(b)(ii)): . . . .

**Box No. VII INTERNATIONAL SEARCHING AUTHORITY**

**Choice of International Searching Authority (ISA)** (if two or more International Searching Authorities are competent to carry out the international search, indicate the Authority chosen; the two-letter code may be used):

ISA / .CN

**Request to use results of earlier search; reference to that search** (if an earlier search has been carried out by or requested from the International Searching Authority):

Date (day/month/year)

Number

Country (or regional Office)

**Box No. VIII DECLARATIONS**

The following declarations are contained in Boxes Nos. VIII (i) to (v) (mark the applicable check-boxes below and indicate in the right column the number of each type of declaration):

Number of  
declarations

- |   |  |   |
|---|--|---|
| <input type="checkbox"/> Box No. VIII (i)   | Declaration as to the identity of the inventor   | : |
| <input type="checkbox"/> Box No. VIII (ii)  | Declaration as to the applicant's entitlement, as at the international filing date, to apply for and be granted a patent             | : |
| <input type="checkbox"/> Box No. VIII (iii) | Declaration as to the applicant's entitlement, as at the international filing date, to claim the priority of the earlier application | : |
| <input type="checkbox"/> Box No. VIII (iv)  | Declaration of inventorship (only for the purposes of the designation of the United States of America)                               | : |
| <input type="checkbox"/> Box No. VIII (v)   | Declaration as to non-prejudicial disclosures or exceptions to lack of novelty   | : |

**Box No. IX CHECK LIST; LANGUAGE OF FILING**

This international application contains:	This international application is accompanied by the following item(s) (mark the applicable check-boxes below and indicate in right column the number of each item):	Number of items
(a) in paper form, the following number of sheets:	1. <input checked="" type="checkbox"/> fee calculation sheet	: 1
request (including declaration sheets) : 4	2. <input checked="" type="checkbox"/> original separate power of attorney	: 1
description (excluding sequence listing and/or tables related thereto) : 11	3. <input type="checkbox"/> original general power of attorney	:
claims : 5	4. <input type="checkbox"/> copy of general power of attorney; reference number, if any: .....	:
abstract : 1	5. <input type="checkbox"/> statement explaining lack of signature	:
drawings : 6	6. <input type="checkbox"/> priority document(s) identified in Box No. VI as item(s): .....	:
<b>Sub-total number of sheets</b> : 27	7. <input type="checkbox"/> translation of international application into (language): .....	:
sequence listing :	8. <input type="checkbox"/> separate indications concerning deposited microorganism or other biological material	:
tables related thereto :	9. <input type="checkbox"/> sequence listing in computer readable form (indicate type and number of carriers)	:
(for both, actual number of sheets if filed in paper form, whether or not also filed in computer readable form; see (c) below)	(i) <input type="checkbox"/> copy submitted for the purposes of international search under Rule 13ter only (and not as part of the international application) :	:
<b>Total number of sheets</b> : 27	(ii) <input type="checkbox"/> (only where check-box (b)(i) or (c)(i) is marked in left column) additional copies including, where applicable, the copy for the purposes of international search under Rule 13ter :	:
(b) <input type="checkbox"/> only in computer readable form (Section 801(a)(i))	(iii) <input type="checkbox"/> together with relevant statement as to the identity of the copy or copies with the sequence listing mentioned in left column :	:
(i) <input type="checkbox"/> sequence listing	10. <input type="checkbox"/> tables in computer readable form related to sequence listing (indicate type and number of carriers)	:
(ii) <input type="checkbox"/> tables related thereto	(i) <input type="checkbox"/> copy submitted for the purposes of international search under Section 802(b-quater) only (and not as part of the international application) :	:
(c) <input type="checkbox"/> also in computer readable form (Section 801(a)(ii))	(ii) <input type="checkbox"/> (only where check-box (b)(ii) or (c)(ii) is marked in left column) additional copies including, where applicable, the copy for the purposes of international search under Section 802(b-quater) :	:
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(additional copies to be indicated under items 9(ii) and/or 10(ii), in right column)		
<b>Figure of the drawings which should accompany the abstract:</b>	<b>Language of filing of the international application:</b> EN	

**Box No. X SIGNATURE OF APPLICANT, AGENT OR COMMON REPRESENTATIVE**

Next to each signature, indicate the name of the person signing and the capacity in which the person signs (if such capacity is not obvious from reading the request).



For receiving Office use only		2. Drawings: <input type="checkbox"/> received:  <input type="checkbox"/> not received:
1. Date of actual receipt of the purported international application: 18 · NOV 2005 (1 8 · 1 1 · 2 0 0 5)		
3. Corrected date of actual receipt due to later but timely received papers or drawings completing the purported international application:		
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5. International Searching Authority (if two or more are competent): ISA /	6. <input type="checkbox"/> Transmittal of search copy delayed until search fee is paid	

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PCT

FEE CALCULATION SHEET

Annex to the Request

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PCT/CN 2005 / 0 0 1 9 6 1  
International Application No.

18 · NOV 2005 (1 8 · 1 1 · 2 0 0 5)  
Date stamp of the receiving Office

Applicant's or agent's  
file reference

FPEL05150060

Applicant

INTEL CORPORATION etc.

CALCULATION OF PRESCRIBED FEES

1. TRANSMITTAL FEE

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2. SEARCH FEE

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International search to be carried out by

CN

(If two or more International Searching Authorities are competent to carry out the international search, indicate the name of the Authority which is chosen to carry out the international search.)

3. INTERNATIONAL FILING FEE

Where items (b) and/or (c) of Box No. IX apply, enter Sub-total number of sheets

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i1 first 30 sheets

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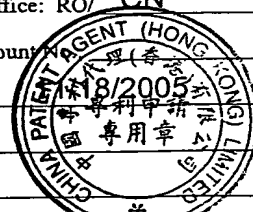
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Name: 中国专利局

Signature: 专用章



## OPERATING MEDIA DEVICES IN PRE-OS ENVIRONMENT

### FIELD

[0001] Embodiments of the invention relate to operating media devices by a computing device, and more particularly to operating media devices by a computing device in a pre-OS environment.

### BACKGROUND

[0002] The essential architecture of computer systems, such as personal computers, includes a central processing unit (CPU) in communication with a system memory that includes a memory medium and a memory controller interface. In addition, a computer system typically includes display interfaces, such as graphical interfaces, for operating video displays and input/output (I/O) control logic for various I/O devices, including a keyboard, mouse, floppy drive, hard drive, etc. An operating system (OS), such as Windows®, typically stored in the memory medium, monitors and conducts operations of the computer system, such as reading instructions and data from I/O devices and system memory. The operating system is typically loaded and executed from the system memory immediately following power-up (i.e. booting).

[0003] While the operating system provides the computer system with the ability to function, it is typically not the only means of booting a computer system. An alternative method commonly known as pre-OS booting, may also be employed to boot up a computer system. As the name suggests, pre-OS booting occurs prior to the loading and execution of the main operating system. In a typical pre-OS booting, a boot image file is accessed, such as from firmware, and executed, which results in the computer system to operate in a pre-OS environment. Due to the limited instructions in the boot image file, however, the computer system's functionality during the pre-OS environment is also typically limited as compared to the functionality of the computer system during executions of a main operating system. One limitation is in the simultaneous operating of two or more media devices, such as video devices.

[0004] Currently, pre-OS firmware does not support the capability for allowing the computer system to interact with multiple devices at the same time. When multiple video



devices are detected, the computer system typically selects one of the devices as the primary device and ignores all others. This is in part due to the limited decode range of the widely used video graphics array (VGA), which restricts the computer system to single VGA device usage, and in part due to computer system's inability to install and dispatch the multiple video option ROMs needed for interacting with multiple video devices at the same time.

DETAILED DESCRIPTION OF THE INVENTION

[0005] The invention may best be understood by referring to the following description and accompanying drawings that are used to illustrate embodiments of the invention.

[0006] FIG. 1 is a block diagram of a computer system in which embodiments of the invention can be practiced.

[0007] FIG. 2 is a flow chart illustrating a process according to an exemplary embodiment of the invention.

[0008] FIGs. 3A-B illustrate block diagrams of a system memory in which embodiments of the invention can be practiced.

[0009] FIGs. 4-6 are flow charts further illustrating the processes according to exemplary embodiment of the invention shown in FIG. 2.

## DETAILED DESCRIPTION

[00010] Embodiments of the invention generally relate to a system and method for operating media devices by a computing device in a pre-OS environment. Herein, an embodiment of the invention may be applicable to media devices used in a variety of computing devices, which are generally considered stationary or portable electronic devices. Examples of a computing device may include, but are not limited or restricted to a computer, a set-top box, video game systems, music playback systems, and the like.

[00011] Reference in the specification to “one embodiment” or “an embodiment” means that a particular feature, structure, or characteristic described in connection with the embodiment is included in at least one embodiment of the invention. The appearances of the phrase “in one embodiment” in various places in the specification are not necessarily all referring to the same embodiment. Some embodiments of the invention are implemented in a machine-accessible medium. A machine-accessible medium includes any mechanism that provides (i.e., stores and/or transmits) information in a form accessible by a machine (e.g., a computer, network device, personal digital assistant, manufacturing tool, any device with a set of one or more processors, etc.). For example, a machine-accessible medium includes recordable/non-recordable media (e.g., read only memory (ROM); random access memory (RAM); magnetic disk storage media; optical storage media; flash memory devices; etc.), as well as electrical, optical, acoustical or other form of propagated signals (e.g., carrier waves, infrared signals, digital signals, etc.), etc.

[00012] In the following description, numerous details are set forth. It will be apparent, however, to one skilled in the art, that the embodiments of the invention may be practiced without these specific details. In other instances, well-known structures and devices are shown in block diagram form, rather than in detail, in order to avoid obscuring the embodiments of the invention.

[00013] Also in the following description are certain terminologies used to describe features of the various embodiments of the invention. For example, the term “media device” refers any on-board or plug-in device, such as video cards, music players, or DVD

players, for example, that is capable of storing video or audio data, such as movies, songs, etc. The term “linear buffer” refers to one or more buffers of a memory system in which obtained data and instructions can be stored. The term “software” generally denotes executable code such as an operating system, an application, an applet, a routine or even one or more instructions. The software may be stored in any type of memory, namely suitable storage medium such as a programmable electronic circuit, a semiconductor memory device, a volatile memory (e.g., random access memory, etc.), a non-volatile memory (e.g., read-only memory, flash memory, etc.), a floppy diskette, an optical disk (e.g., compact disk or digital versatile disc “DVD”), a hard drive disk, or tape. The term “pre-OS” (also known as pre-boot) environment refers to a setting in which tasks are performed before a main operating system (OS) is loaded, and may include limited use of a disk operating system (DOS).

[00014] With reference to FIG. 1, an embodiment of an exemplary computer environment is illustrated. In an exemplary embodiment of the invention, a computing device 100, such as a personal computer, comprises a bus 105, such as a Peripheral Component Interconnect (PCI) bus for example, or other communication medium for communicating information. A processor 111 is coupled to the bus 105 for processing information.

[00015] The computing device 100 further comprises a system memory 140 which comprises a main memory 143, such as random access memory (RAM) or other dynamic storage device as for storing information and instructions to be executed by the processor 111. Main memory 143 also may be used for storing temporary variables or other intermediate information during execution of instructions by the processor 111. The system memory 140 also may comprise a read only memory (ROM) 144 and/or other static storage devices 145 for storing static information and instructions for the processor 111, such as magnetic disk or optical disc and its corresponding drive, flash memory or other nonvolatile memory, or other memory device. Such elements may be combined together or may be separate components, and utilize parts of other elements of the computing device 100.

[00016] The system memory 140 also comprises a memory decoder 142 for translating between the addresses and storage format used by the processor 111 and the format used

by memory chips and modules of memory system 140, such as main memory 115, ROM 144 and storage devices 145. The system memory 140 also comprises a memory controller 141 which controls the memory system 140 and a memory mapping logic 146 which may also be implemented in hardware or software stored in system memory 140. System memory 140 may also include other forms of storage (not shown) such as registers, caches etc.

[00017] The computing device 100 may also be implemented with a display device 130 coupled to a bus 105, such as a liquid crystal display (LCD) or other display technology, for displaying information to an end user. In some environments, the display device 130 may be a touch-screen that is also utilized as at least a part of an input device. In other environments, display device 130 may be or may include an auditory device, such as a speaker for providing auditory information.

[00018] Other devices included are media devices 160, shown as media device \_1 through media device \_N ( $N > 1$ ), which are devices capable of storing video or audio data, such as movies, songs, etc. The media device 160 communicates with the processor 111, and may further generate its results on the display device 130. A communication device 150 may also be coupled to the bus 105. Depending upon the particular implementation, the communication device 150 may include a transceiver, a wireless modem, a network interface card, or other interface device. The computing device 100 may be linked to a network or to other devices using the communication device 150, which may include links to the Internet, a local area network, or another environment. In an embodiment of the invention, the communication device 150 may provide a link to a service provider over a network. The computing device 100 also includes input/output (I/O) decoder 120, display interface decoder 135 and control logic 170 (stored in hardware or software) whose functions are described in greater detail in conjunction with FIGs. 3-6 below.

[00019] FIG. 2 is a flow chart illustrating a process according to an exemplary embodiment of the invention. The elements of the computing system 100 shown in FIG. 1 are referenced in conjunction with FIG. 2 for illustrative purposes. As shown in FIG. 2, following an action to start the process (block 200), a plurality of media

devices 160 in communication with the computing device 100 are selected (block 210) following detection. Suitably at least one of the media devices 160 comprises an on-board device and a plug-in device, such as video devices (e.g., a video cards), audio devices (e.g, digital music players), and audio/video devices (e.g., DVD players). Next, communication resources, such as PCI bus resources, for the media devices 160 selected by the computing device are allocated and programmed (block 220).

[00020] The selected media devices 160 are then initialized (block 230), each at a different time period, as described in greater detail in conjunction with FIGs. 3-6 below. The information corresponding to each initialized media device 160 is then mapped (block 240) to different memory locations of the computing device 100 by the memory mapping logic 146, as described in greater detail in conjunction with FIGs. 3-6 below. Next, the computer system 100 operates, such as interacts with, the initialized media devices 160 based on the mapped information corresponding to each operated media device 160 (block 250). The operating of the initialized media devices 160 is performed while the computing device is in a pre-OS environment. The process then ends (block 260).

[00021] The overall operations of the FIG. 2 will now be illustrated in further detail in conjunction with exemplary circuit block diagrams of FIGs. 3A-B and flow charts of FIG. 4-6. Referring to FIGs. 3A-B, for simplicity only two media devices 160, such as media device\_1 and media device\_2 are shown although embodiments of the invention are not limited to only two media devices 160. As shown in FIGs. 3A-B, a memory region 147 is selected by the system memory 140, such as in the main memory 143 or the storage device 145. The memory region 147 comprises addresses, such as from 0 to 5 GB. At the lower end of the addresses, such as 0 to C0000, are the I/O region 147c and memory region 147d in which is stored I/O and general information of a video graphics array (VGA) interface, such as a Legacy VGA interface. At the upper end of the addresses, such as 3GB and higher, are a set of memory locations 147a, such as line fill buffers (LFB), such as LFB\_1 and LFB\_2. In an exemplary embodiment of the invention, the number of memory locations 147a

used corresponds to number of the media devices 160 selected. In the example of FIGs. 3A-B, only two line fill buffers, LFB\_1 and LFB\_2 are shown since only two media devices 160, media device\_1 and media device\_2, are selected, for simplicity.

[00022] FIG. 4 is an exemplary flow chart further illustrating the initializing and mapping processes used in FIG. 2 (blocks 230, 240) in the context of the simplified example of FIGs. 3A-B when only two media device 160, media device\_1 and media device\_2 are selected. As shown in FIG. 4, the process starts (block 400) and proceeds to initialize a first media device 160, such as media device\_1, during a first time period (block 410) as described in greater detail in conjunction with FIG. 5 below. Information corresponding to the initialized media device\_1 is then mapped (shown symbolically by line 300 in FIG. 3A) by the memory mapping logic 146 to a memory location 147a corresponding to the media device\_1, such as to LFB\_1 (block 420). Next, a second media device 160, such as media device\_2, is initialized (block 430) during a second time period that is subsequent to the first time period corresponding to the initialization of media device\_1, as described in greater detail in conjunction with FIG. 6 below. Information corresponding to the initialized media device\_2 is then mapped (shown symbolically by line 305 in FIG. 3B) by the memory mapping logic 146 to a memory location 147a corresponding to the media device\_2, such as to LFB\_2 (block 440). The flow is then returned (block 450) to FIG. 2 (block 240).

[00023] FIG. 5 is an exemplary flow chart further illustrating the initializing process for a first media device 160 illustrated in block 410 of FIG. 4. As shown in FIG. 5, the process starts (block 500) and proceeds to enabling a decoding of a display interface on a path of the media device\_1, including all upstream buses 105, such as PCI buses (block 510). In an exemplary embodiment of the invention, the display interface comprises a video graphics array (VGA) interface, and the VGA decoding on the path of the media device\_1 is performed by the display interface decoder 135 (shown in FIG. 1). Next, input/output decoding is enabled for the media device\_1 (block 520), such as by media device\_1, such as by using the Input/Output decoder 120 shown in FIG. 1. The input/output decoding is performed on the information stored in the I/O region 147c of

FIG. 3A as described above.

[00024] A memory decoding is then enabled for the media device<sub>1</sub> (block 530), such as by using the memory decoder 142 shown in FIG. 1. The memory decoding is performed on the information stored in the memory region 147d of FIG. 3A as described above. Service instructions corresponding to the media device<sub>1</sub> are thereafter loaded and dispatched, such as by the memory controller 141 from the ROM 144 (block 540). The service instructions corresponding to the media device<sub>1</sub> may for instance include video service instructions, audio service instructions or both. For instance, the video service instructions may comprise option ROM instructions, such as option ROM<sub>1</sub> shown in FIG. 3A, stored in the ROM 144 and loaded into memory region 147b. A memory information and a mode corresponding to a memory location 147a, such as memory location LFB<sub>1</sub>, is then obtained by the memory controller 141 (block 550). The media device<sub>1</sub> is thereafter switched by the control logic 170 to the mode obtained for the media device<sub>1</sub> (block 560). The flow is then returned (block 570).

[00025] As described above in conjunction with FIG. 4 (block 420), the information corresponding to the initialized media device<sub>1</sub> is mapped (shown symbolically by line 300 in FIG. 3A) to the memory location LFB<sub>1</sub> (obtained in block 550 of FIG. 5). The information corresponding to the initialized media device<sub>1</sub> may for instance include instructions and/or addresses corresponding to the media device<sub>1</sub> and those stored in regions 147b (such as ROM<sub>1</sub>), 147c and 147d. Since the media device<sub>1</sub> was previously switched to an operation mode corresponding to the memory location LFB<sub>1</sub> (FIG. 5, block 560), the media device<sub>1</sub> will from thereon use the mapped information residing in memory location LFB<sub>1</sub> to interact with the computing device 100 (shown symbolically by line 301 in FIG. 3A), instead of interacting using the information stored in regions 147b, 147c and 147d (shown symbolically by line 302 in FIG. 3A).

[00026] FIG. 6 is an exemplary flow chart further illustrating the initializing process for a second media device 160 illustrated in blocks 430 of FIG. 4. As shown in FIG. 6, the process starts (block 600) and proceeds to disabling the enabled



decoding of the display interface on the path of the media device\_1, as previously illustrated in block 510 of FIG. 5 (block 610). The enabled input/output decoding for the media device\_1 and the enabled memory decoding for the media device\_1 are then disabled (blocks 620, 630). The input/output decoding and memory decoding for the media device\_2 is enabled for the media device\_2 (blocks 640, 650).

[00027] Following the foregoing enablement and disablement operation of block 610-650, the service instructions corresponding to the media device\_2 are loaded and dispatched by memory controller 141 (block 660). According to an exemplary embodiment, the service instructions corresponding to the media device\_2 comprises video service instructions, audio service instructions or both. The video service instructions may for instance comprise an option ROM instructions, such as option ROM\_2 shown in FIG. 3B, stored in ROM 144 (FIG. 1) and loaded into memory region 147b. The storage of option ROM\_2 instructions in the region 147b will partially or fully overwrite the previously stored option ROM\_1 instructions in the region 147b. This however, will not affect the operation of the media device\_1, since as described above the information needed for the operating of media device\_1 by the computing device 100 is now residing in LFB\_1. Next, a memory information and a mode corresponding to another memory location 147a, such a memory location LFB\_2, is obtained by the memory controller 141 (block 670) and the media device\_2 is switched by the control logic 170 to the mode obtained for the media device\_2 (block 680). In an exemplary embodiment of the invention, the memory decoding for the media device\_1 is re-enabled by the memory decoder 142 following the initialization of media device\_2. The flow is then returned (block 690) to FIG. 4 (block 430).

[00028] As described above in conjunction with FIG. 4 (block 440), the information corresponding to the initialized media device\_2 is mapped (shown symbolically by line 305 in FIG. 3B) to the memory location LFB\_2 (obtained in block 670 of FIG. 6). The information corresponding to the initialized media device\_2 may for instance include instructions and/or addresses corresponding to media device and those stored in regions 147b (such as ROM\_2), 147c and 147d. The flow is then returned (block 450) to FIG. 2

(block 240). Since the media device\_2 was previously switched to an operation mode corresponding to the memory location LFB\_2 (FIG. 6, block 680), the media device\_2 will from thereon use the mapped information residing in memory location LFB\_2 to interact with the computing device 100 (shown symbolically by line 304 in FIG. 3B), instead of interacting using the information stored in regions 147b, 147c and 147d (shown symbolically by line 303 in FIG. 3A). The regions 147b, 147c and 147d can then be freed for initialization of additional selected media devices 160 in the manner described above.

[00029] In this way, as previously described in conjunction with FIG. 2 (block 250) the computing device 100 can separately operate and interact in a pre-OS environment with each of the media device\_1 and media device\_2. Thus, so long as each media device 160 is initialized at a different time period and set to a mode corresponding to the initialized media device 160, then line frame buffers 147a corresponding to each initialized media device 160 can be used to interact with each initialized media device 160 at the same time in a pre-OS environment, regardless of the information stored in regions 147b, 147c and 147d.

[00030] In an exemplary embodiment of the invention, the software that, if executed by a computing device 100, will cause the computing device 100 to perform the above operations described in conjunction with FIGs. 2-6 is stored in a storage medium, such as main memory 143, and storage devices 145. Suitably, the storage medium is implemented within the processor 111 of the computing device 100.

[00031] It should be noted that the various features of the foregoing embodiments were discussed separately for clarity of description only and they can be incorporated in whole or in part into a single embodiment of the invention having all or some of these features.

## CLAIMS

What is claimed is:

1. A method comprising:  
initializing a plurality of media devices in communication with a computing device;  
mapping information corresponding to each initialized media device to a plurality of memory locations of the computing device; and  
operating the initialized media devices based on the mapped information corresponding to each operated media device while the computing device is in a pre-OS environment.
2. The method of claim 1, wherein each media device is initialized at a different time period corresponding to each initialized media device.
3. The method of claim 1, wherein each information corresponding to each initialized media device is mapped to a different memory location in the plurality of memory locations of the computing device.
4. The method of claim 1, further comprising:  
initializing a first media device in the plurality of media devices by the computing device during a first time period;  
mapping a first information corresponding to the initialized first media device to a first memory location in the plurality of memory locations of the computing device;  
initializing a second media device in the plurality of media devices by the computing device during a second time period;  
mapping a second information corresponding to the initialized second media device to a second memory location in the plurality of memory locations of the computing device; and  
operating the initialized first and second media devices based on the mapped first and second information while the computing device is in the pre-OS environment.

5. The method of claim 4, wherein the initializing a first media device by the computing device comprises:
  - enabling a decoding of a display interface on a path of the first media device;
  - enabling input/output decoding for the first media device;
  - enabling memory decoding for the first media device;
  - loading and dispatching a service instructions corresponding to the first media device;
  - obtaining a first memory information and a first mode corresponding to the first memory location; and
  - switching the first media device to the first mode.
6. The method of claim 5, wherein the initializing a second media device by the computing device comprises:
  - disabling the enabled decoding of the display interface on the path of the first media device;
  - disabling the enabled input/output decoding for the first media device;
  - disabling the enabled memory decoding for the first media device;
  - enabling input/output decoding for the second media device;
  - enabling memory decoding for the second media device;
  - loading and dispatching a services instructions corresponding to the second media device;
  - obtaining a second memory information and a second mode corresponding to the second memory location; and
  - switching the second media device to the second mode.
7. The method of claim 4, wherein operating the first and second media devices based on the mapped first and second information comprises:
  - re-enabling memory decoding for the first media device; and
  - interacting with the first and second media devices in the first and second modes.

8. The method of claim 1, further comprising:  
selecting a plurality of media devices in communication with the computing device for initializing; and  
allocating and programming communication resources for the selected plurality of media devices by the computing device prior to the initializing the plurality of media devices.
9. A system comprising:  
a plurality of media devices in communication with a computing device and adapted for initialization by the computing device; and  
a memory mapping logic adapted to map information corresponding to the initialized media devices to a plurality of memory locations in a system memory of the computing device,  
wherein the computing device is adapted to operate the initialized media devices based on the mapped information corresponding to each operated media device while the computing device is in a pre-OS environment.
10. The system of claim 9, further comprising:  
a display interface decoder subsystem adapted to decode a display interface on a path of each media device and to be enabled and disabled;  
an input/output decoder subsystem adapted to decode input/output for each media device, and to be enabled and disabled;  
a memory decoder subsystem adapted to decode memory instructions for each media device, and to be enabled and disabled;  
a memory controller adapted to load and dispatch service instructions stored in the system memory, and to obtain a memory information and a mode corresponding to each memory location corresponding to each media device;  
and  
a control logic adapted to switch the each media device to a mode corresponding to the switched media device.

11. The system of claim 10, wherein each of the service instructions corresponding to each media device comprises at least one of a video service instructions and an audio service instructions.

12. The system of claim 11, wherein the video service instructions comprises an option ROM instructions, and wherein the display interface comprises a video graphics array (VGA) interface.

13. The method of claim 9, wherein the memory mapping logic is further adapted to map each information corresponding to each initialized media device to a different memory location in the plurality of memory locations of the computing device.

14. The system of claim 9, wherein at least one of memory location in the plurality of memory locations comprises a linear frame buffer.

15. The system of claim 9, wherein the information corresponding to each initialized media device comprises at least one of data, instructions, and addresses.

16. The system of claim 9, wherein the computing device is adapted to detect the media devices; and to allocate and program communication resources for the detected media devices prior to the initialization of at least one of the media devices.

17. The system of claim 9, wherein at least one of the media devices comprises an on-board device and a plug-in device, wherein at least one of the on-board device and a plug-in device comprises at least one of a video device, an audio device and a audio/video device.

18. A storage medium that provides software that, if executed by a computing device, will cause the computing device to perform the following operations:

initializing a plurality of media devices in communication with the computing device; and

operating the plurality of initialized media devices while the computing device is in a pre-OS environment.

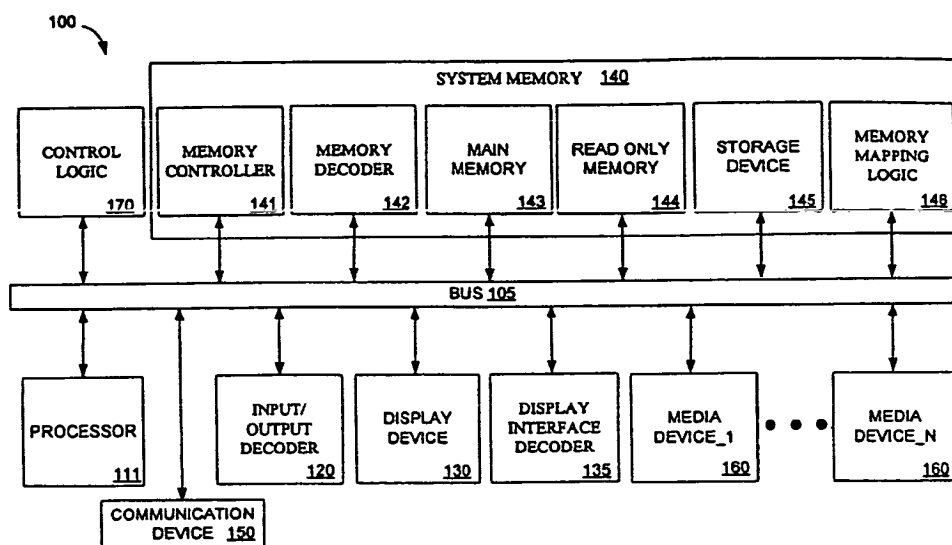
19. The storage medium of claim 18, wherein each media device is initialized at a different time period corresponding to each initialized media device.

20. The storage medium of claim 18 further comprising software adapted to map information corresponding to each of the plurality of initialized media devices to a plurality of memory locations of the computing device, each of the plurality of memory locations being different memory locations.

**ABSTRACT**

According to one embodiment, a method for initializing a plurality of media devices in communication with a computing device; mapping information corresponding to each initialized media device to a plurality of memory locations of the computing device; and operating the initialized media devices based on the mapped information corresponding to each operated media device while the computing device is in a pre-OS environment. According to another embodiment a system comprising a plurality of media devices in communication with a computing device and adapted for initialization by the computing device; and a memory mapping logic adapted to map information corresponding to the initialized media devices to a plurality of memory locations in a system memory of the computing device, wherein the computing device is adapted to operate the initialized media devices based on the mapped information corresponding to each operated media device while the computing device is in a pre-OS environment.





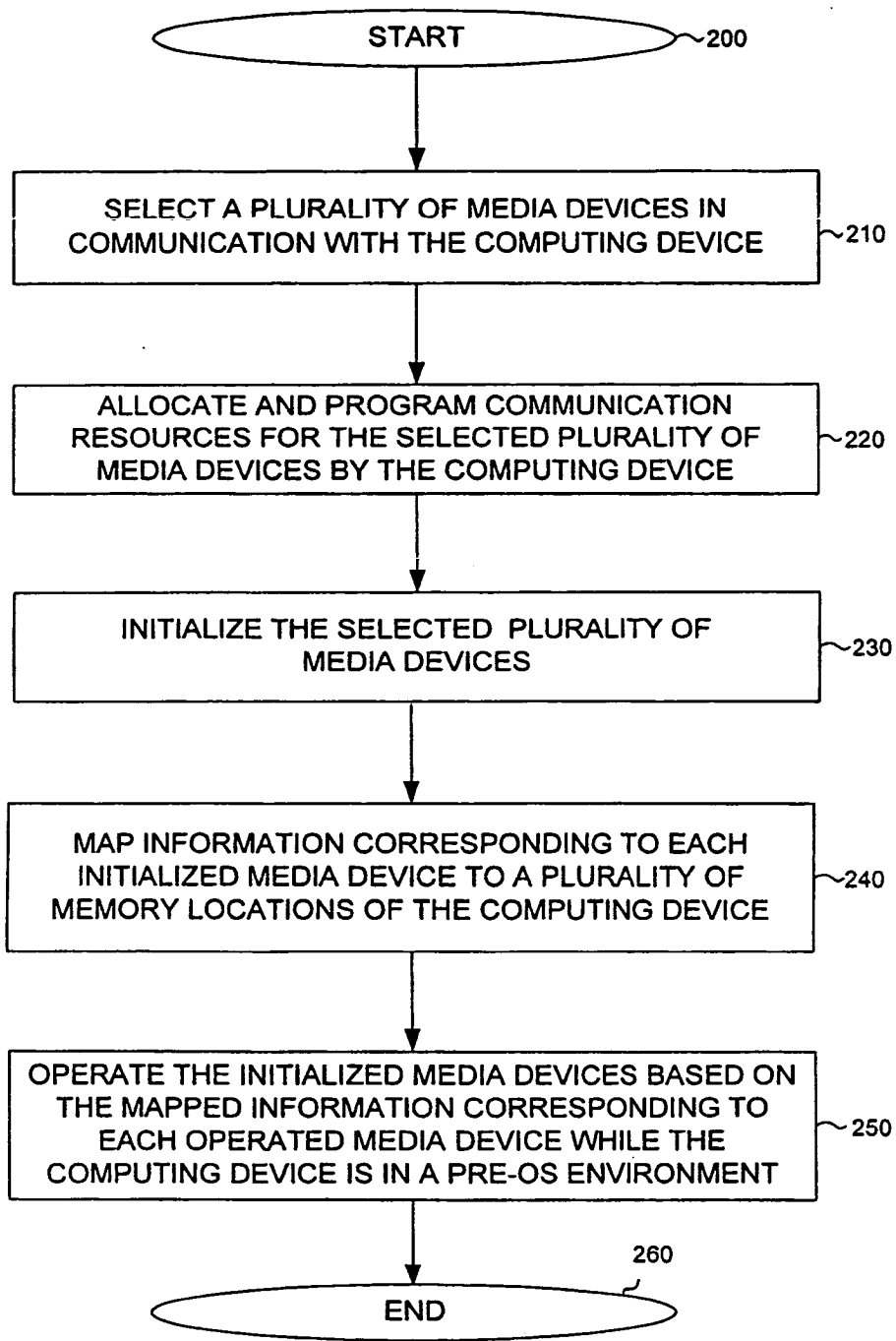


FIG. 2

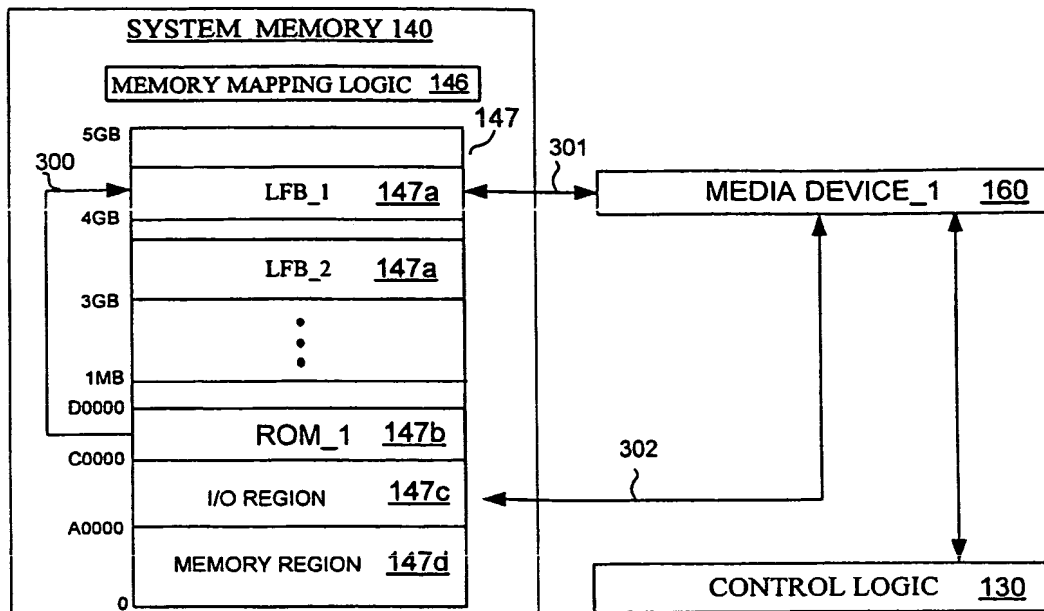


FIG. 3A

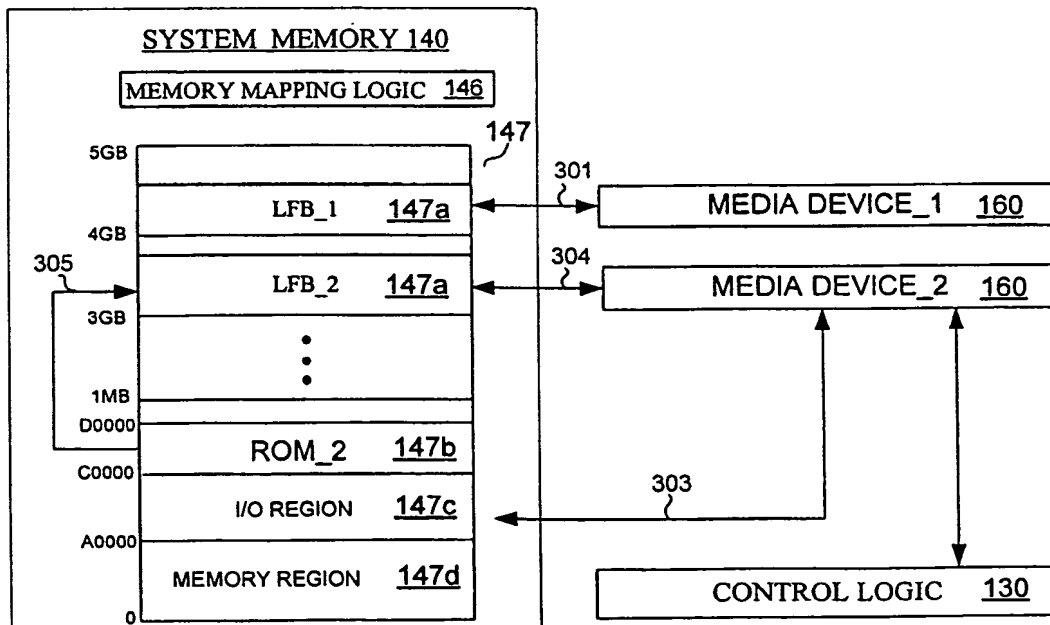


FIG. 3B

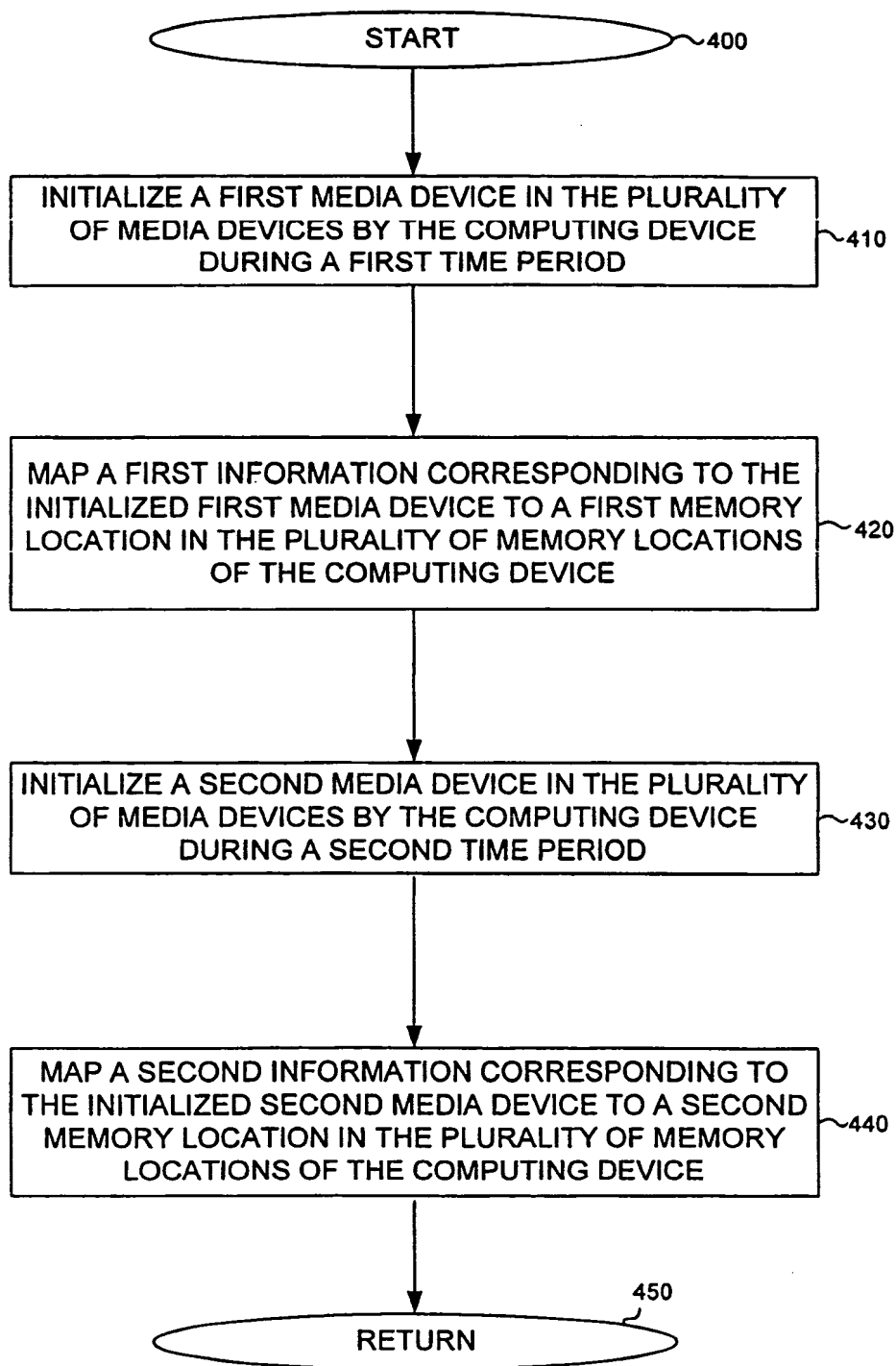


FIG. 4

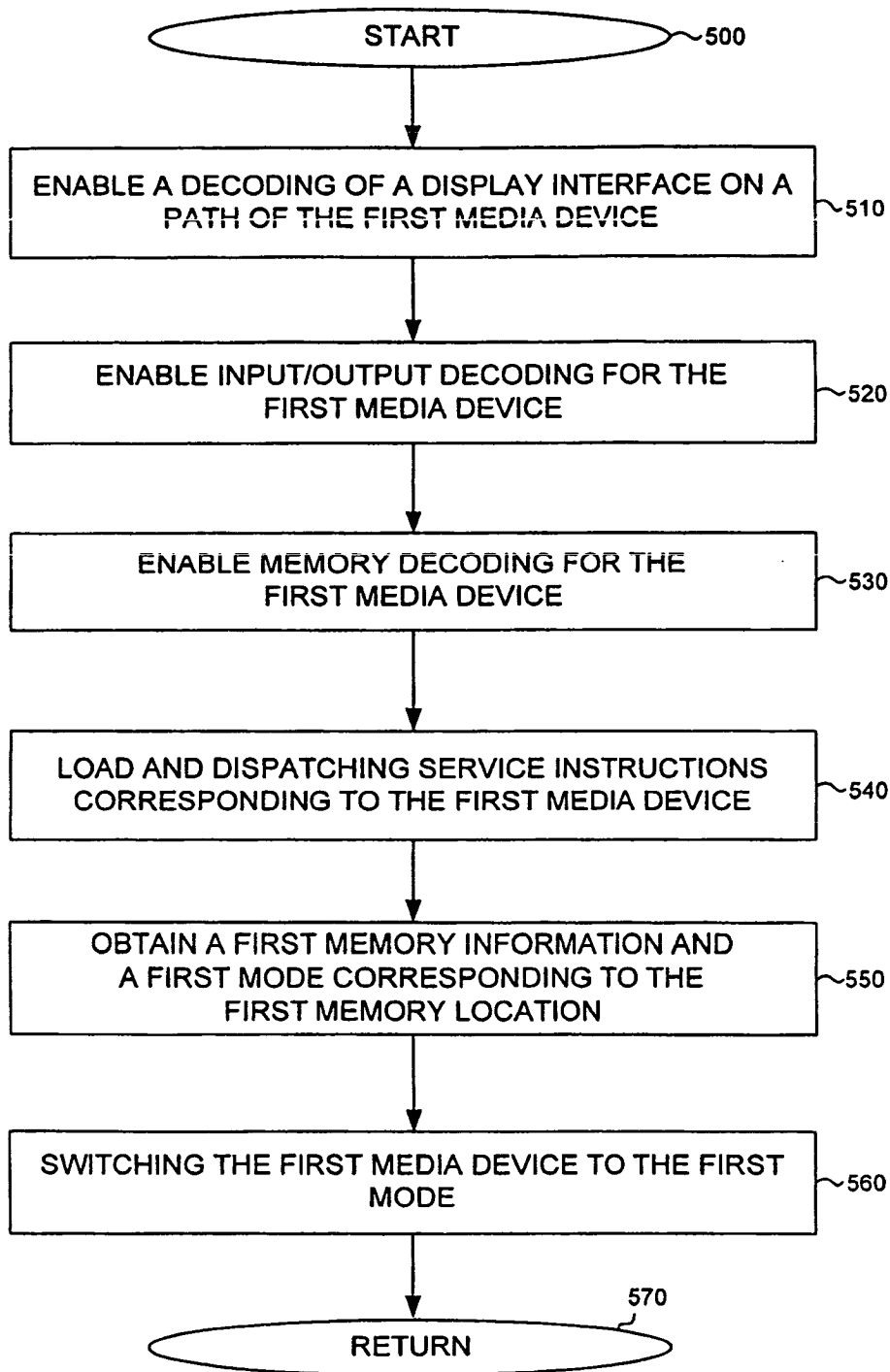


FIG. 5

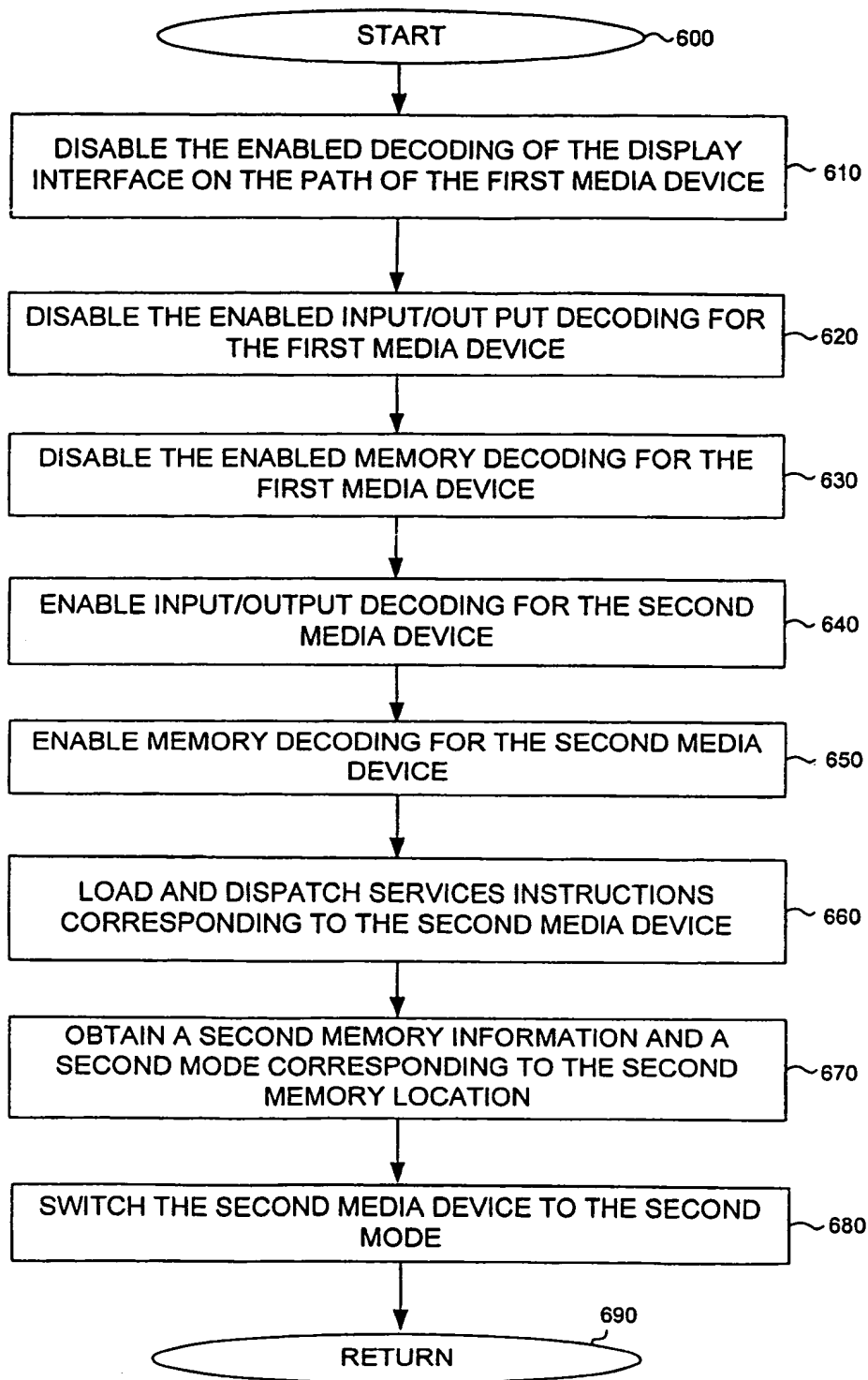


FIG. 6